RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA
University Institute of Technology,
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ADVERTISEMENT FOR WALK-IN-INTERVIEW

Applications are invited through walk-in-interview for a Junior Research Fellow at consolidated fellowship of Rs. 15000 under a research project funded by MPCST, Bhopal. Qualification for the post of Junior Research Fellow is B.E./B.Tech. degree in Electronics/Mechanical Engineering. Preference will be given to GATE qualified candidates with experience in relevant research area. Interview will be held on **16th August 2022** at **11.00 AM** in Deptt. of Electronics & Communication Engineering, University Institute of Technology, Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal. Other details can be seen at **www.uitrgpv.ac.in** and **www.rgpv.ac.in**

M.P. Madhyam/105924/2022   **DIRECTOR**

Department of Electronics and Communication Engineering

University Institute of Technology
Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal
Airport Bypass Road, Bhopal, Madhya Pradesh -462033

Advertisement for the post of Junior Research Fellow (JRF)

Applications are invited for filling up of the post of Junior Research Fellow (JRF) in a research project titled "Intelligent fault diagnosis of industrial machines using deep learning techniques", sponsored by Madhya Pradesh Council of Science and Technology (MPCST), Bhopal, Madhya Pradesh.

Description of the Project:
The objective of this project is to develop an intelligent fault detection and failure prevention scheme for industrial machinery. This basically is condition-based machine health monitoring in which the machine parameters are continuously monitored and evaluated. The methodology aims to utilize machine learning approach which takes effective measures to isolate or repair the fault thus preventing machine failure by early fault diagnosis.

Details of the post:
No. of vacancies: 01
Project Duration: 1.5 years
Fellowship: Rs. 15,000 per month

Eligibility:
  Essential Qualification:
   B.Tech/B.E. degree in Electronics/Mechanical Engineering.
   Preference will be given to GATE qualified candidates with experience in relevant research area.
  Desirable:
   The applicants are desired to know MATLAB programming and basic concepts of digital signal processing and machine learning.

Date of Interview: 16th August 2022, 11 AM
Venue: Department of Electronics and Communication Engineering, UIT-RGPV, Bhopal
How to apply?
   1. Candidates are required to submit their details via google form latest by **14th August 2022**.
      Google form link:
   2. Candidates who have filled the Google form may appear in the walk-in-interview on **16th August 2022** along with all the original documents, CV and attested copies of relevant certificates/marksheets, experience certificates, etc.

Contact details of the Principal Investigator of the project

Dr. Preety D Swami
Professor, Department of Electronics and Communication Engineering
UIT-RGPV, Bhopal
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